

BIBLIOGRAFIA

- [Alv.89] Alvarez, A. R., *BiCMOS Technology and Applications*, Kluwer Academic Publishers, Boston, 1989.
- [Ann.86] Annaratone, M., *Digital CMOS Circuit Design*, Kluwer Academic Publishers, Boston, 1986.
- [Ban.89] Banzhaf, W., *Computer-Aided Circuit Analysis using SPICE*, Prentice-Hall, Englewood Cliffs, New Jersey, 1989.
- [Ber.92] Bergé, J. M. Bergé, Fonkoua, A., Maginot, S. and Rouillard, J., *VHDL Designer's Reference*, Kluwer Academic Publishers, 1992.
- [Bor.87] Borrione, D., *From HDL Descriptions to Guaranteed Correct Circuit Design*, North Holland, Amsterdam, 1987.
- [Bra.89] Brayton, R. K., Hachtel, G. D., McMullen, C. T. and Sangiovanni-Vincentelli, A. L., *Logic Minimization Algorithms for VLSI Synthesis*, Kluwer Academic Publishers, Boston, 1989.
- [Bro.92] Brodersen, R. W., *Anatomy of a Silicon Compiler*, Kluwer Academic Publishers, Boston, 1992.
- [Che.00] Chen, W. K., *The VLSI Handbook*, IEEE Press, New York, 2000.
- [Del.89] Delgado-Frías, J. and Moore, W. R., *VLSI for Artificial Intelligence*, Kluwer Academic Publishers, Boston, 1989.
- [Den.85] Denyer, P. and Renshaw, D., *VLSI Signal Processing: A bit-Serial Approach*, Addison-Wesley, Workingham, England, 1985.
- [Dil.88] Dillinger, T. E., *VLSI Engineering*, Prentice-Hall, Englewood Cliffs, New Jersey, 1988.
- [Eng.86] Engl, W. L., *Process and Device Modeling*, North Holland, Amsterdam, 1986.
- [Fab.90] Fabricius, E. D., *Introduction to VLSI Design*, McGraw-Hill International Editions, New York, 1990.
- [Fic.87] Fichtner, W. and Morf, M., *VLSI CAD Tools and Applications*, Kluwer Academic Publishers, Boston, 1987.

- [Gaj.92] Gajski, D., Dutt, N., Wu, A. and Lin, S., *High-Level Synthesis*, Kluwer Academic Publishers, Boston, 1992.
- [Gei.90] Geiger, R. L., Allen, P. E. and Strader, N. R., *VLSI Design Techniques for Analog and Digital Circuits*, McGraw-Hill, New York, 1990.
- [Gla.85] Glasser, L. A. and Dobberpuhl, D. W., *The Design and Analysis of VLSI Circuits*, Addison-Wesley, Reading, MA, 1985.
- [Got.86] Goto, S., *Design Methodologies*, Ed., North Holland, Amsterdam, 1986.
- [Gre.86] Gregorian, R. and Temes, G., *Analog MOS Integrated Circuits*, John Wiley & Sons, New York, 1986.
- [Har.91] Harr, R. E. and Stanculescu, A. G., *Applications of VHDL to Circuit Design*, Kluwer Academic Publishers, Boston, 1991.
- [Har.93] Harrold, S. J., *GaAs IC Design*, Prentice-Hall, New York, 1993.
- [Has.88] Haskard, M. R. and May, I. C., *Analog VLSI Design*, Prentice-Hall, New York, 1988.
- [Hei.88] Heinbuch, D. V., *CMOS3 Cell Library*, Addison-Wesley, Reading, MA, 1988.
- [Hil.87] Hill, D. D. and Coelho, D. R., *Multi-level Simulation for VLSI Design*, Kluwer Academic Publishers, Boston, 1987.
- [Hör.86] Hörbst, E., *Logic Design and Simulation*, North Holland, Amsterdam, 1986.
- [Kan.99] Kang, S. M. and Leblebici, Y., *CMOS Digital Integrated Circuits. Analysis and Design*, McGraw-Hill, Boston, 1999.
- [Maz.92a] Maziasz, R. L. and Hayes, J. P., *Layout Minimization of CMOS Cells*, Kluwer Academic Publishers, Boston, 1992.
- [Maz.92b] Mazor, S. and Langstraat, P., *A Guide to VHDL*, Kluwer Academic Publishers, Boston, 1992.
- [Mea.80] Mead, C. and Conway, L., *Introduction to VLSI Systems*, Addison-Wesley, Reading, MA, 1980.
- [Mea.89] Mead, C., *Analog VLSI and Neural Systems*, Addison Wesley, Reading, Massachusetts, 1989.
- [Mic.92] Michel, P., Lauther, U. and Duzy, P., *The Synthesis Approach to Digital Systems Design*, Kluwer Academic Publishers, Boston, 1992.
- [Mil.84] Millman, J., *Microelectrónica*, Ed. Hisp.-Europ., Barcelona, 1984.

- [Mil.86] Milne, C. J. and Subrahmanyam, P. A., *Formal Aspects of VLSI Design*, North Holland, Amsterdam, 1986.
- [Muk.86] Mukherjee, A., *Introduction to nMOS and CMOS VLSI Systems Design*, Prentice-Hall, Englewood Cliffs, N. J., 1986.
- [Nav.93] Navabi, Z., *VHDL: Analysis and Modeling of Digital Systems*, McGraw-Hill, New York, 1993.
- [Nea.92] Neamen, D. A., *Semiconductor Physics and Devices, Basic Principles*, Irwin, Homewood, Illinois, 1992.
- [New.83] Newkirk, J. A. and Mathews, R., *The VLSI Designer's Library*, Addison-Wesley, Reading, Massachusetts, 1983.
- [Nis.86] Nishizawa, J., *Semiconductor Technologies*, North Holland, Amsterdam, 1986.
- [Oht.86] Ohtsuki, T., *Layout Design and Verification*, North Holland, Amsterdam, 1986.
- [Pre.88] Preas, B. and Lorenzetti, M., *Physical Design Automation of VLSI Systems*, The Benjamin/Cummings Publishing Company, Menlo Park, California, 1988.
- [Puc.88] Pucknell, D. A. and Eshraghian, K., *Basic VLSI Design*, Prentice-Hall, New York, 1988.
- [Rao.89] Rao, V. B., Overhauser, D. V., Trick, T. N. and Hajj, I. N., *Switch-Level Timing Simulation of MOS VLSI Circuits*, Kluwer Academic Publishers, Boston, 1989.
- [Rub.87] Rubin, S. M., *Computer Aids for VLSI Design*, Addison-Wesley, Reading, Massachusetts, 1987.
- [Rue.86] Ruehli, A. E., *Circuit Analysis, Simulation and Design*, North Holland, Amsterdam, 1986.
- [Sha.98] Sharma, A. K., *Programmable Logic Handbook*, McGraw-Hill, New York, 1998.
- [Str.96] Streetman, B. G., *Solid State Electronic Devices*, Prentice-Hall, Englewood Cliffs, N. J., 1996.
- [Tho.90] Thomas, D. E., Lagnese, E. D., Walker, R. A., Nestor, J. A., Rajan, J. V. and Blackburn, R. L., *Algorithmic and Register-Transfer Level Synthesis: The Architect's Workbench*, Kluwer Academic Publishers, Boston, 1990.
- [Tho.91] Thomas, D. E. and Moorby, P., *The Verilog Hardware Description Language*, Kluwer Academic Publishers, Boston, 1991.

[Tri.87] Trimberger, S. M., *An Introduction to CAD for VLSI*, Kluwer Academic Publishers, Boston, 1987.

[Tru.86] Trullemans, C., *Algorithmics for VLSI*, Academic Press, London, 1986.

[Ull.84] Ullman, J. D., *Computational Aspects of VLSI*, Computer Science Press, Rockville, Maryland, 1984.

[Uye.92] Uyemura, J. P., *Circuit Design for CMOS VLSI*, Kluwer Academic Publishers, Boston, 1992.

[Wes.93] Weste, N. and Eshraghian, K., *Principles of CMOS VLSI Design: A Systems Perspective*, Addison-Wesley, Reading, MA, 1993.

[Wil.86] Williams, T. W., *VLSI Testing*, North Holland, Amsterdam, 1986.